

## Foreword

This book contains papers presented at the 11th Symposium on Computer Arithmetic at Windsor, Ontario, Canada, on June 29 through July 2, 1993. The Symposia on Computer Arithmetic are held every other year and have alternated between North America and Europe since the 6th Symposium in 1983. This dynamic gathering attracts prestigious researchers from around the world.

The enlightening and, in many cases, cutting-edge contributions to the present Symposium represent a wide spectrum of interests ranging from number systems to VLSI-oriented algorithms for arithmetic operations. A total of 34 papers were accepted for presentation in 10 technical sessions at ARITH 11. These 34 papers were selected from 79 papers submitted — 42 to Mary Jane Irwin, the US coordinator; 33 to Simon Knowles, the European coordinator; and four to Naofumi Takagi, the Asian coordinator. The coordinators skillfully organized and managed the review process for the papers submitted to them and each paper was reviewed by at least three referees before the program committee meeting in Detroit, Michigan, on January 9-10, 1993. It was a very difficult task for the committee to select the 34 papers ultimately accepted for publication out of the numerous excellent submissions. Further reviews were obtained from the 23 program committee members present at the meeting in Detroit.

Decisions on papers authored or co-authored by program committee members present were made in two subcommittees chosen so that no members could influence the decision concerning their own papers. Some papers were conditionally accepted subject to a revision and another reviewing cycle. The authors of such conditionally accepted papers were very cooperative in expediting revisions satisfying the referees.

We want to express our sincere thanks to the referees, the members of the Program Committee, and especially to Simon Knowles and Naofumi Takagi for a thorough and timely job in the evaluation and selection process. Thanks are also due to the IEEE Computer Society, the Technical Committee on VLSI, the Department of Computer Science at The Pennsylvania State University, the Office of Research Services, the Academic Vice-President at the University of Windsor, and the Natural Sciences and Engineering Research Council of Canada for their sponsorship and support.

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