IEEE COMPUTER SOCIETY SIXTH SYMPOSIUM ON COMPUTER ARITHMETIC

FOREWORD

This symposium is the sixth in a series of meetings sponsored by the IEEE Computer Society, dedicated to the understanding, development and applications of computer arithmetic. Previous meetings have all been held in the U.S., but in recognition of an increasing international participation it was decided, at the fifth symposium, to emphasize the true international spirit of the meetings, by scheduling the sixth meeting to take place outside the U.S. From the contents of these proceedings it can be noticed that contributions have come from almost all parts of the world, but still with a significant number of papers from the U.S.

As defined by the topics covered in this and previous meetings, computer arithmetic is a discipline which draws upon mathematics, computer science and electrical engineering, in an effort to understand and improve the technology of the arithmetic units of computers. Part of the efforts of this discipline has been, and is being devoted to arithmetic on the »usual« fixed- and floating-point radix representations; but much effort has been applied to »non-standard« number representations and their associated arithmetic, as well as to the user interface. Contributions to the proceedings also cover topics from numerical error control, language aspects, and methods from the field of symbolic and algebraic computations, indicating useful interaction with the user communities.

The organizers of this symposium would like to express their gratitude towards the Computer Society of IEEE, the Technical Committee on Computer Architecture (TCCA), for their sponsorship, and towards Aarhus University for hosting and supporting the meeting.

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