

Foreword

The 8th Symposium on Computer Arithmetic (ARITH8) was held at the Centro Alessandro Volta in Como, Italy on May 19-21, 1987. The Symposia on Computer Arithmetic are held every other year, most recently alternating between the United States and Europe. A total of 36 papers were presented in ten technical sessions at ARITH8. Contributions were made by 68 authors from all over the world. Papers published in the Proceedings cover topics from theoretical foundations, to arithmetic algorithms and processors, to fault-tolerant arithmetic, to nonstandard number representations and their associated arithmetic.

A total of 83 papers were submitted to this year's Symposium. Each paper was reviewed by at least three referees. The program committee met on December 13 to select the final papers. Since only about two out of five papers were accepted, many worthwhile contributions were excluded by necessity. The quality of a symposium such as this is ultimately dependent on the manuscripts received and the care with which the reviewers fulfill their responsibilities. We would like to thank all the authors, referees, and program committee members for their contributions and timely efforts in promoting the scientific quality of this Symposium.

Thanks are also due to the IEEE Computer Society, the IEEE Technical Committee on Computer Architecture, the IEEE Technical Committee on VLSI, the IEEE North Italy Section, and The Pennsylvania State University for their sponsorship and to the Politecnico di Milano for hosting and supporting this Symposium. Also, for the first time, ARITH8 was held in cooperation with IFIP's Working Group 2.5.

In conclusion, it has been a pleasure and an honor to serve as co-program chairmen of the 8th Symposium on Computer Arithmetic. We hope you will benefit from the presentations at the Symposium and the papers appearing in this Proceedings.

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