

## Foreword

The 9th Symposium on Computer Arithmetic (ARITH9) was held in Santa Monica, California, on September 6-8, 1989. The Symposia on Computer Arithmetic are held every other year, involving the researchers from around the world. A total of 30 papers were presented in ten technical sessions at ARITH9. The contributions to the present Symposium represent a wide spectrum of interests ranging from number systems to VLSI-oriented algorithms for arithmetic operations to error detection.

It is a pleasure to express our appreciation to the members of the Program Committee and the referees whose thorough and prompt responses were critical to the quality of the Symposium. A total of 61 papers were submitted to this year's Symposium. Each paper was reviewed by at least three referees without knowing the names and affiliation of the authors. The Program Committee met on March 6, 1989, and made the final selection of 30 papers after a thorough deliberation. The Program Committee appreciates the interest and efforts of all authors.

Thanks are also due to the IEEE Computer Society, the IEEE Technical Committee on Computer Architecture, the Technical Committee on VLSI, and the University of California at Los Angeles, Computer Science Department, for their sponsorship. This Symposium was held in cooperation with IFIP's Working Group 2.5.

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